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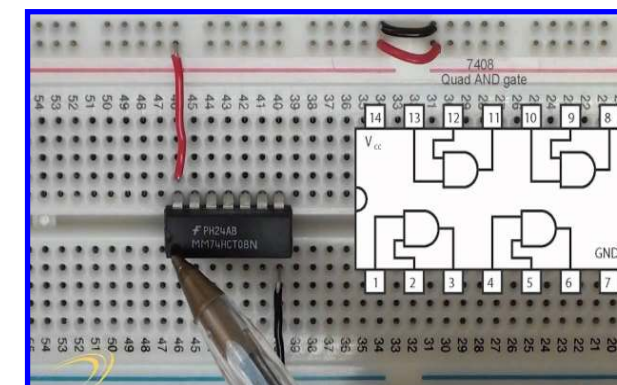
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PROBABILITY APPROACHES TO BASIC LOGIC GATES

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ABSTRACT:

Basic logic gates are the fundamental building blocks of logic design . These logic gates are used to design combinational logic circuits for various practical applications. Sequential logic circuits have been designed by incorporating various memory elements to the existing combinational logic circuits. Applications of Probability concepts have been mingled with our day-to-day real life operations.

Abstract Report: The Title Accurately Said The Study was About.

INTRODUCTION:

Logic gates are building blocks to the entire domain of digital logic gates. These are NOT, AND , OR , NAND (AND + NOT) , NOR (OR + NOT) , XOR (EXCLUSIVE OR) , XNOR (EXCLUSIVE NOT OR) gates. Among these NOT , AND and NOR are called BASIC GATES / FUNDAMENTAL GATES. NAND and NOR gates are applications of AND and OR Gates respectively .The remaining gates are Exclusive gates. Each gate has its own course of action and applications.

Introduction Report: This Article Include Full Introduction, Methods, Results & Introduction Section.

OVERVIEW:

- Discussion.1: NOT Gate
- Discussion.2: AND Gate
- Discussion.3: OR Gate
- Discussion.4: NAND Gate
- Discussion.5: NOR Gate
- Discussion.6: XOR Gate
- Discussion.7: XNOR Gate

Overview Report: Figures are Imported to Provide Explanation for Background Information. Conclusion of This Paper Clearly Supported Results.

CONCLUSION:

In the same way many other relations can also be discussed and applied for advanced concepts. The mapping between Logic Design and probability concepts is not one to one. But here the main attempt is to get an one to one mapping between a maximum number of Probability applications and Digital Logic Design concepts.

Conclusion Report: The Text is Rounded off with a Conclusion that Discusses the Implication of The Findings & Ideas Discussed & Their Impact on Future Research Direction.

REFERENCES:

- Alan Clements,Principles of computer hardware. second edition oxford science publications.
- M.M. Mano, Digital Design, Prentice Hall.

Reference Report: There are Places where the Author D. V. S. S. Subrahmanyam Need to Cite a Reference, but Have Not

SUMMARY OF ARTICLE

	Very High	High	Average	Low	Very Low
1. Interest of the topic to the readers	✓				
2. Originally & Novelty of the ideas	✓				
3. Importance of the proposed ideas		✓			
4. Timelines		✓			
5. Sufficient information to support the assertions made & conclusion drawn					
6. Quality of writing(Organization, Clarity, Accuracy Grammer)	✓				
7. References & Citation(Up-to-date, Appropriate Sufficient)			✓		

This Article is Innovative & Original, No Plagiarism Detected

FUTURE RESEARCH SUGGESTIONS

This Article can expand further research for MINOR/MAJOR Research Project at UGC

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